

| | | | | |
|-----------------------------------|---------------------------------------|--|--|-------------|
| Notice of References Cited | Application/Control No. 10/710,889 | | Applicant(s)/Patent Under Reexamination CHANG, HUI-HUANG | |
| | Examiner Henry Yu | | Art Unit 2112 | Page 1 of 1 |

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|-----------------------|----------------|
| * | A | US-4,222,103 | 09-1980 | Chamberlin, George P. | 713/600 |
| * | B | US-5,701,514 | 12-1997 | Keener et al. | 710/14 |
| * | C | US-5,727,170 | 03-1998 | Mitchell et al. | 710/105 |
| * | D | US-6,026,453 | 02-2000 | Craft, David John | 710/62 |
| * | E | US-6,038,400 | 03-2000 | Bell et al. | 710/11 |
| * | F | US-6,088,754 | 07-2000 | Chapman, John T. | 710/305 |
| * | G | US-6,138,177 | 10-2000 | Williams et al. | 710/8 |
| * | H | US-6,668,301 B2 | 12-2003 | Bacigalupo, Tommaso | 710/305 |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
| | N | | | | | |
| | O | | | | | |
| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|--|
| | U | Chung, Ki-Seok et al. "An Algorithm for Synthesis of System-Level Interface Circuits." Proceedings of the 1996 IEEE/ACM international conference on Computer-aided design. January 1997. |
| | V | |
| | W | |
| | X | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.